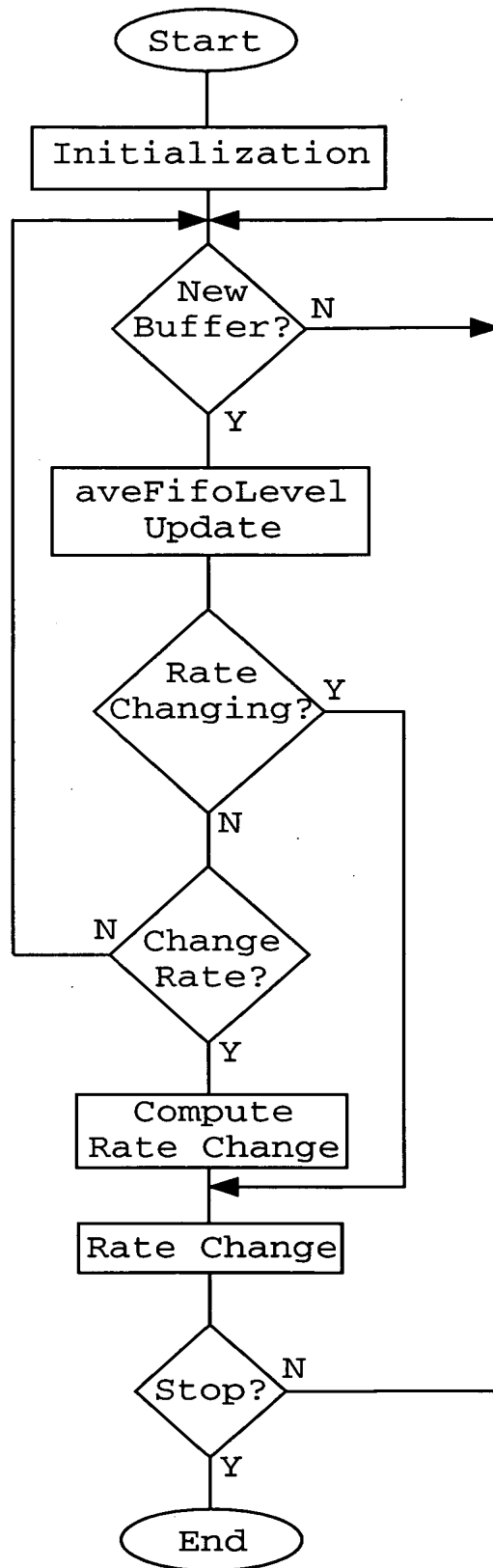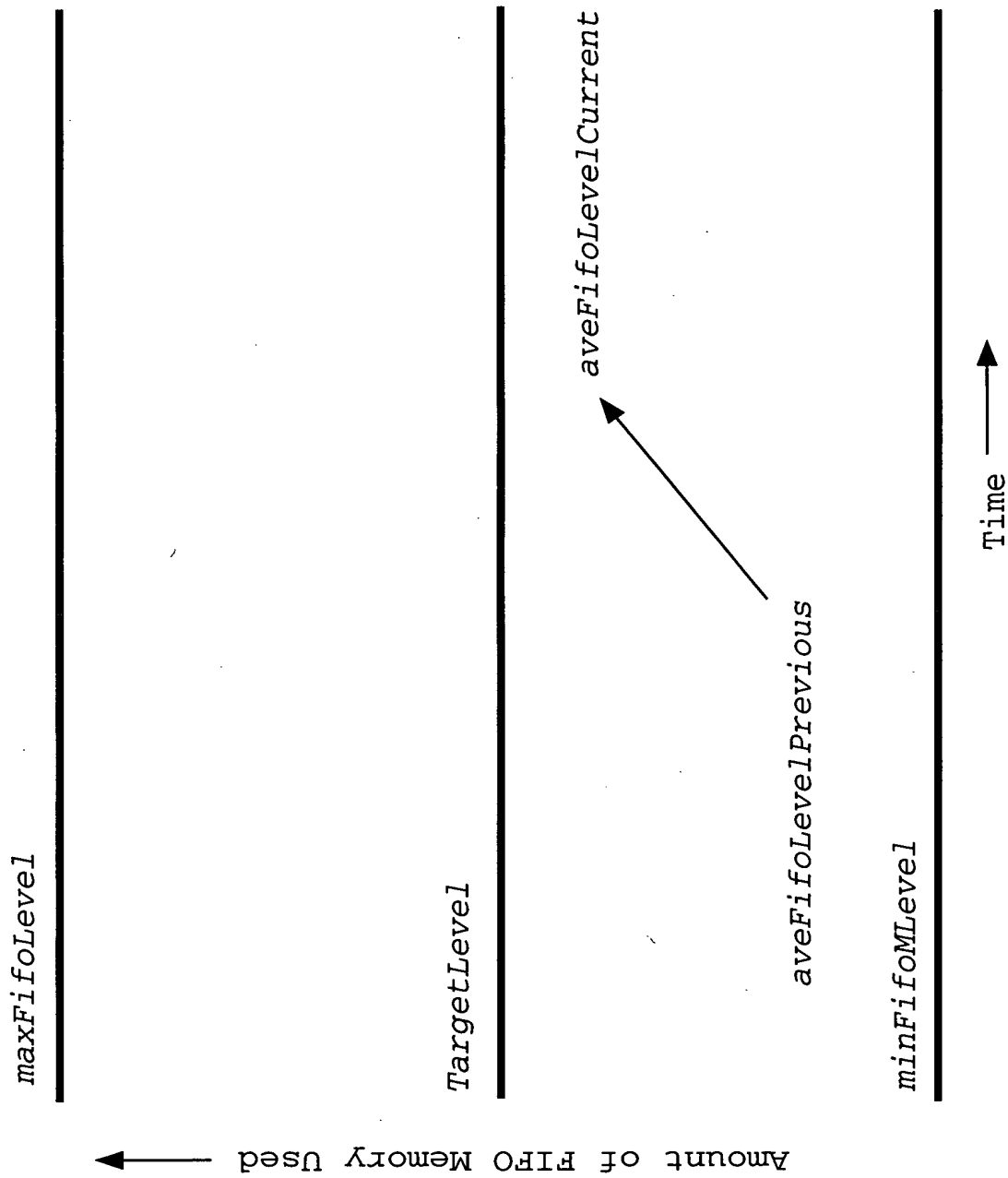Figure 1

Figure 2

Figure 3
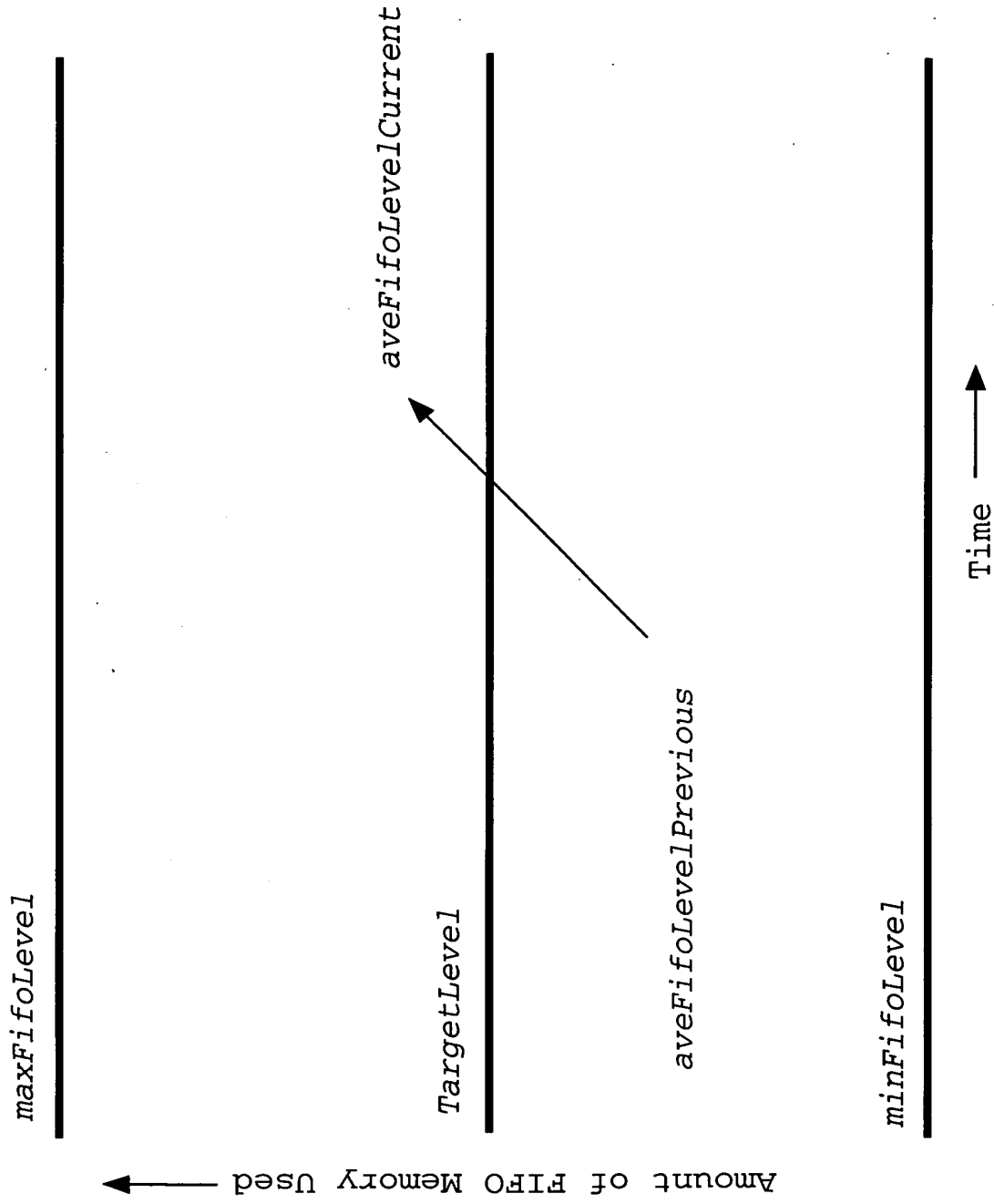
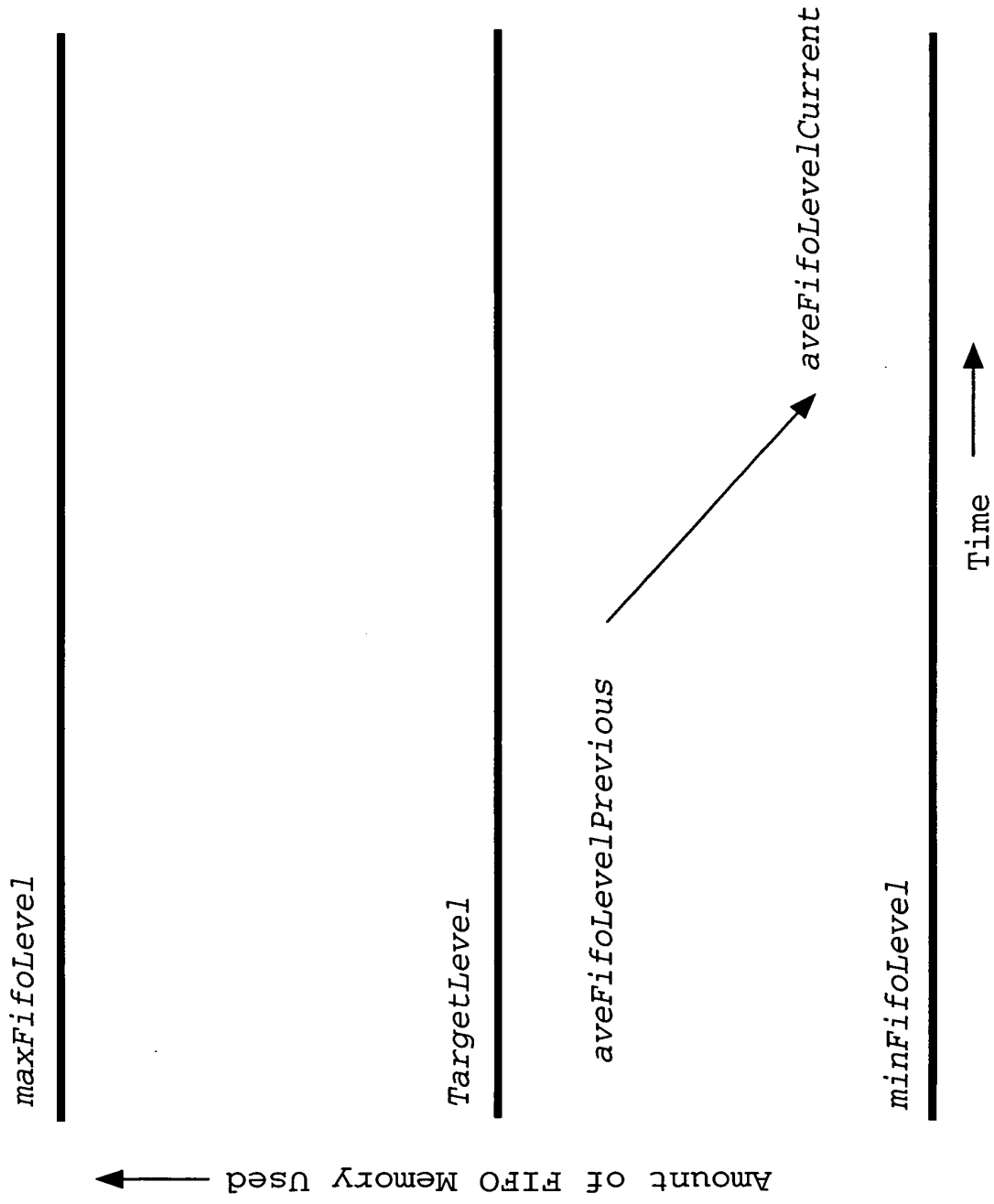**Figure 4**

maxFifoLevel

TargetLevel

aveFifoLevelPrevious

aveFifoLevelCurrent

minFifoLevel

Amount of FIFO Memory Used

Time

Figure 5

maxFifoLevel

Network Jitter

TargetLevel

Clock Mismatch

Network Jitter

minFifoLevel

Amount of FIFO Memory Needed

Time →

Figure 6